



Applicant:

Rost et al.

Art Unit:

2811

Serial No.:

10/696,816

Examiner:

Crane, S.

Filing Date:

10/30/2003

Docket No.:

TI-35257

Customer No.: 23494

Conf. No.:

1301

Title: ONE MASK HIGH DENSITY CAPACITOR FOR INTEGRATED CIRCUITS

## **ELECTION**

MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(A)

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as first Class Mail in an envelope addressed to Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450.

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Marianna Smith

Dear Sir:

With respect to the Restriction Requirement mailed on 12/15/2004, the Examiner has restricted the instant application to the invention of Group I (Claims 1-10), or Group II (Claims 11-20). In light of this, Applicants elect to pursue Group I, (Claims 1-10) without traverse.

Respectfully submitted,

acqueline J. Garner Attorney for Applicants Reg. No. 36,144

Texas Instruments Incorporated P.O. Box 655474, M/S 3999 Dallas, TX 75265 (214) 532-9348 or (972) 917-5643 Fax: (972) 917-4418